

高知工科大学 理工学群

教員公募

- 1 公募人員 助教（大学支援研究員） 1名
- 2 所 属 高知工科大学 理工学群
- 3 勤 務 地
（雇入れ直後）高知工科大学 香美キャンパス（高知県香美市土佐山田町宮ノ口 185）
（変更の範囲）変更の予定なし
- 3 募集背景 高知工科大学理工学群 曲研究室では、酸化物半導体薄膜の合成および薄膜トランジスタ等のデバイス作製に関する研究開発に従事する助教（大学支援研究員）1名を募集します。助教（大学支援研究員）は、教育や大学運営に関する業務を担当せず、研究に専念できる職です。採用後は、本学教員である曲が主宰する先進エレクトロニクス研究室の研究プロジェクトに参画していただきます。また、本人の希望や研究計画に応じて、上記プロジェクトに従事するだけでなく、自律的に自身の研究テーマ・プロジェクトを立案し、推進することも可能です。
- 4 研究内容 次世代半導体材料として注目されている酸化物半導体を対象に、スパッタリング法や原子層堆積法などを用いた薄膜合成、薄膜トランジスタ等のデバイス作製、ならびに薄膜物性およびデバイス特性の評価・解析に関する研究を行います。薄膜合成からデバイス作製、特性評価・解析までを一貫して行い、酸化物半導体デバイスに関する基盤技術の研究開発を推進していただきます。
- 5 応募資格 固体物理学、材料科学、半導体工学、薄膜工学、またはこれらに関連する研究分野における研究実績を有し、博士の学位を取得している方、または着任時までに取得見込みの方。
- 6 着任時期 令和9年4月1日以降のできるだけ早い時期（応相談）
- 7 待 遇 等
任 期：3年以内
※採用から2年目に実施する審査の結果、卓越した成果が継続して期待できると判断された場合は再任（2年以内）を認めます。それ以降、再任はありません。
※本学在籍中において本学の別公募への応募を妨げません
- 給 与：年俸制（420万円、再任後年俸460万円、宿舍手当 30万円、退職手当あり、通勤手当あり）、赴任旅費支給あり
個人研究費：研究奨励費50万円／年額
勤務形態：同意に基づき、専門業務型裁量労働制適用（みなし労働時間1日7時間 45分）
裁量労働制を適用しない場合は、週5日8時30分～17時15分勤務（休憩1時間）
休 日：土日、祝日法に基づく休日、12月29日～1月3日
保 険 等：公立学校共済組合加入、雇用保険・地方公務員災害補償制度加入
- 8 提出書類（電子メール添付にてご送付ください）
以下資料について、電子データをお送りください。選考の過程で追加の書類の提出をお願いする場合があります。

- (1) 履歴書（以下の URL から指定の様式をダウンロードして使用すること）
<https://www.kochi-tech.ac.jp/disclosure/corporate/post-4.html>
- (2) 学位取得を証明するもの（在学中の場合は、修了見込証明書もしくは指導教員の推薦書等）
- (3) これまでの研究の概要（A4 判 1 枚程度）
- (4) 研究業績リスト（査読付き学术论文、総説、著書、受賞、国際会議発表、招待講演、学協会活動等に分類し、「西暦」表記かつ直近のものから順に記載）
論文、総説は「論文タイトル、著者名、掲載雑誌名、発行年、巻、開始と終了ページ」の順に省略せずに書き、責任著者に＊を付けること。
- (5) 今後の研究計画（A4 判 1 枚程度）
- (6) 応募者に関して所見を伺える方 1 名の氏名、所属及び連絡先

※応募書類は 1 つの pdf ファイルにまとめてご提出ください。

※応募書類は本公募の審査以外の目的には一切使用しません。

9 選考 書類選考後、面接（オンライン含む）を実施します。

10 応募期限

令和 8 年 11 月 30 日（月）必着

※適任者の採用が決まり次第、募集を締め切ります。

11 書類提出先及び問い合わせ先

高知工科大学 理工学群 講師 曲 勇作

E-mail: magari.yusaku[at]kochi-tech.ac.jp （[at]を@に置き換えてください）

（提出いただいた書類は本公募の審査目的以外には使用いたしません。審査終了後は責任をもって破棄いたします。）

参考URL: <https://www.kochi-tech.ac.jp/profile/ja/magari-yusaku.html>

Recruitment of Research Associate (Post-doctoral Researcher)
School of Engineering Science, Kochi University of Technology (Kochi, Japan)
(Kochi Prefectural Public University Corporation)

1. Number of positions

Research Associate (Post-doctoral Researcher): 1 position

2. Affiliation

School of Engineering Science, Kochi University of Technology

3. Place of work

[Immediately after hiring] Kochi University of Technology, Kami Campus (185 Miyanokuchi, Tosayamada, Kami City, Kochi Prefecture)

[Scope of change] No changes planned

4. Background of recruitment

The Magari Laboratory in the School of Engineering Science at Kochi University of Technology invites applications for one Research Associate (Post-doctoral Researcher) to conduct research and development on the synthesis of oxide semiconductor thin films and the fabrication of devices such as thin-film transistors. This position allows the appointee to focus on research, with no teaching or university administrative duties. The appointee will participate in research projects in the Advanced Electronics Laboratory led by Yusaku Magari, a faculty member of the University. Depending on their interests and research plans, the appointee may also independently formulate and pursue their own research topics and projects in addition to participating in these projects.

5. Fields of expertise

Oxide semiconductors, thin-film synthesis, and semiconductor devices; solid-state physics, materials science, semiconductor engineering, thin-film engineering, and related fields.

6. Job description

Research will focus on oxide semiconductors, which are attracting attention as next-generation semiconductor materials. Activities include thin-film synthesis using sputtering, atomic layer deposition, and related methods; fabrication of devices such as thin-film transistors; and evaluation and analysis of thin-film physical properties and device characteristics. The appointee will carry out the full sequence from thin-film synthesis through device fabrication to characterization and analysis, advancing research and development of foundational technologies for oxide semiconductor devices.

Assigned courses:

None. This position carries no teaching or university administrative duties.

7. Application qualifications

Applicants must satisfy all of the following requirements.

- (1) Hold a Ph.D. degree, or be expected to obtain one by the date of appointment.
- (2) Have a research track record in solid-state physics, materials science, semiconductor engineering, thin-film engineering, or a related research field.

8. Start of appointment

As soon as possible on or after April 1, 2027 (negotiable).

9. Benefits, etc.

Term of appointment: Up to 3 years.

Contract renewal:

Reappointment for up to 2 years is possible if a review conducted in the second year of employment determines that continued outstanding achievements can be expected. No further reappointment is available.

The appointee may apply for other advertised positions at the University during their employment.

Salary: Annual salary of JPY 4,200,000, increasing to JPY 4,600,000 upon reappointment. Housing allowance: JPY 300,000. Retirement allowance and commuting allowance are provided. Relocation travel expenses are also provided.

Work hours: With the appointee's consent, the professional discretionary work system applies (deemed working hours: 7 hours and 45 minutes per day). If this system does not apply, working hours are 8:30 to 17:15, five days a week, with a one-hour break each day.

Holidays: Saturdays, Sundays, public holidays specified by the Public Holidays Act, and December 29 through January 3.

Insurance, etc.: Membership in the Public School Mutual Aid Association, and coverage under employment insurance and workers' accident compensation insurance.

Individual research funding: Research incentive funding of JPY 500,000 per year.

10. Documents to be submitted

Please submit the following documents electronically as email attachments (see "11. How to submit documents"). Additional documents may be requested during the selection process.

- (1) Curriculum vitae (download and use the designated form from <https://www.kochi-tech.ac.jp/disclosure/corporate/post-4.html>).
- (2) Proof of degree acquisition (if currently enrolled, a certificate of expected completion, a letter of recommendation from the academic supervisor, or equivalent documentation).
- (3) Summary of previous research (approximately one A4 page).
- (4) List of research achievements, categorized into peer-reviewed journal articles, review articles, books, awards, international conference presentations, invited lectures, academic society activities, etc. Use Gregorian calendar years and list entries in reverse chronological order, with the most recent first. For journal and review articles, provide all details without abbreviation in the following order: article title, author names, journal name, year of publication, volume, and first and last pages. Mark the corresponding author with an asterisk (*).
- (5) Future research plan (approximately one A4 page).
- (6) Name, affiliation, and contact information of one person who can provide comments on the applicant.

Please combine all application documents into a single PDF file.

Application documents will be used solely for screening for this position.

11. How to submit documents

Please send the application documents as a single PDF attachment by email to the contact listed in "14. Contact information".

12. Application deadline

Applications must be received by Monday, November 30, 2026. Recruitment will close as soon as a suitable candidate has been selected.

13. Selection method

Applicants who pass the document screening will be interviewed. Interviews may be conducted online.

14. Contact information

Application submissions and inquiries:

Yusaku Magari, Lecturer, School of Engineering Science, Kochi University of Technology

Email: magari.yusaku[at]kochi-tech.ac.jp (replace [at] with @)

Reference URL: <https://www.kochi-tech.ac.jp/profile/en/magari-yusaku.html>

Submitted documents will be used solely for screening for this position and will be securely disposed of after the selection process is completed.