Highly Flexible Integrated System and its Development Platform

Project Leader
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1. Objective
This project is aimed at:
(a) Development of a novel multi-grained reconfigurable architecture
(b) VLSI implementation of the proposed reconfigurable architecture
(c) Development of CAD tools for implementing user applications on our reconfigurable system

2. Project Outline
To that end, the project will consist of the following phases:
(a) Development of a novel multi-grained reconfigurable architecture
(b) VLSI implementation of the proposed reconfigurable architecture
(c) Development of CAD tools for implementing user applications on our reconfigurable system

3. Expected Performance
In this project, the successful candidate would be expected to:
(a) Working independently with the supervision of the project leader
(b) Assist the senior members with the development of our reconfigurable system
(c) Provide supervision of younger members for developing our reconfigurable system

4. Required Skills and Knowledge
The successful candidate for this project will have the following knowledge and skills:
(a) Computer architecture and FPGA architecture
(b) HDL design and FPGA implementation
(c) C and/or C++ language for computer programming

References

See our admission guidelines:
https://www.kochi-tech.ac.jp/english/admission/ssp_aft19oct/ssp_application_guideline.html

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